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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cheng-Lien Chiang
Assignee: Bridge Semiconductor Corporation
Title: METHOD OF MAKING A SEMICONDUCTOR PACKAGE
DEVICE THAT INCLUDES A CONDUCTIVE TRACE WITH
RECESSED AND NON-RECESSED PORTIONS
Serial No.: 10/059,686 Filed: January 29, 2002
Examiner: Zarneke, D. Group Art Unit: 2827
Atty. Docket No.: BDG005-1

COMMISSIONER FOR PATENTS
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Alexandria, VA 22313-1450

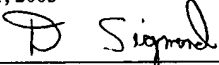
INFORMATION DISCLOSURE STATEMENT

Pursuant to Applicant's duty of disclosure under 37 C.F.R. § 1.56 and §§ 1.97-1.98, Applicant hereby submits the enclosed Form PTO-1449.

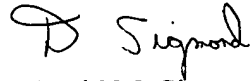
Copies of documents cited on the enclosed Form PTO-1449 are not enclosed because they were previously cited by or submitted to the U.S. Patent Office in prior U.S. Application Serial No. 10/042,812 filed January 9, 2002. The above-identified application is a continuation of the prior application.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, an admission that any of these documents, alone or in any combination, is considered to be material to patentability, an admission that any of these documents is prior art as to the above-identified application, or an admission against interest in any manner.

This Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits. Accordingly, no fee is due.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 1, 2003	
	5, 1, 03
David M. Sigmond Attorney for Applicant	Date of Signature

Respectfully submitted,



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Form PTO-1449 U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No. BDG005-1		Serial No. 10/059,686	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)					Applicant Cheng-Lien Chiang			
					Filing Date January 29, 2002		Group Art Unit 2812	
U.S. Patent Documents								
*Examiner Initial	AA	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	3,627,901	12/1971	Happ	174	52 PE		
	AB	3,678,385	07/1972	Bruner	324	158 F		
	AC	3,930,114	12/1975	Hodge	174	52 FP		
	AD	4,506,238	03/1985	Endoh et al.	333	138		
	AE	5,157,480	10/1992	McShane et al.	357	74		
	AF	5,207,102	05/1993	Takahashi et al.	73	727		
	AG	5,436,500	07/1995	Park et al.	257	696		
	AH	5,677,566	10/1997	King et al.	257	666		
	AI	5,866,939	02/1999	Shin et al.	257	666		
	AJ	5,894,107	04/1999	Lee et al.	174	52.2		
	AK	5,951,804	09/1999	Kweon et al.	156	244.12		
	AL	6,114,770	09/2000	Akram et al.	257	784		
	AM	6,130,116	10/2000	Smith et al.	438	127		
	AN	6,232,152	05/2001	DiStefano et al.	438	124		
	AO	6,297,543	10/2001	Hong et al.	257	666		
	AP	6,303,997	10/2001	Lee	257	778		
	AQ	6,326,700	12/2001	Bai et al.	257	790		
	AR	6,445,077	09/2002	Choi et al.	257	786		
	AS	6,455,356	09/2002	Glenn et al.	438	123		
	AT	6,468,836	10/2002	DiStefano et al.	438	128		
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AU	JP-01128897	05/1989	Japan	B42D	15/02		X
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your next communication to Applicant.								

